



Many ideas one solution

The modules **MMfpgaX2** and evaluation board **EVBfpga** are delivered with exemplary software, written in VHDL language with use of Xilinx WebPack environment. Exemplary software includes:

MMfpga02 and MMfpga12:

7seg display - 7-Seg display test, flashing sing PROPOX with f=1Hz
(short on 7-Seg display)

Buzzer - buzzer test, signal frequency f=1[Hz]
(short on buzzer pin)

LEDs and Switches - switches and LED diodes test
(short on leds,SW7,SW6,SW5,SW4 pins)

LCD - shows text on LCD with use of 4-bit mode
(short on LCD pins - no short on VGA and 7-Seg pins !!!)

VGA - test monitor on VGA. Can be display single color and saturation changing:

dip-switch 1 - black
dip-switch 2 - cyan
dip-switch 3 - magenta
dip-switch 4 - blue
dip-switch 5 - yellow
dip-switch 6 - green
dip-switch 7 - red

Saturation changing SW7(+1), SW6(-1)
(short on VGA,SW7,SW6 pins)

VGA_LOGO - PROPOX logo on VGA, we can manipulate it:
- SW7(up)
- SW6(down)
- SW5(left)
- SW4(right)
(short on VGA and SW7,SW6,SW5,SW4 pins)

VGA_TAPETA - B/W 512x256 wallpaper with company name.
(short on VGA)

MMfpga12:

PS2 - read chars form keyboard (chars A-Z) and display it on LCD.
(cables - PS2_1_D->J1_5 , PS2_1_C->J1_6)

RS232_PS2 - read chars from keyboard (chars A-Z and space) and display it on hyperterminal.
(cables - PS2_1_D->J1_5 , PS2_1_C->J1_6)
(short on RS232 pins - J2_21 , J2_22)

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